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discrete topology on n-dimensional square - N-dimensional square tessellated grids,10.1016/j.imavis.2004.06.005,Image and Vision Computing,Pieter P. Jonker. Morphological image processing: architecture

morphological image processing using custom instructions on - Title: MORPHOLOGICAL IMAGE PROCESSING USING CUSTOM INSTRUCTIONS ON DISTRIBUTED NIOS PROCESSORS Author: hcren Last modified by: hcren Created Date

vlsi focal-plane array processor for - morphological image processing VLSI design for high-speed morphological image processing is developed by combining a two-dimensional fine-grain parallel array

morphological operations in recursive - This paper discusses the use of recursive neighbourhoods in mathematical From the field of VLSI Design P.P Jonker; Morphological Image Processing:

architectures for digital optical image processing - Architectures For Digital Optical Image Processing Using Morphological Filters. processing architecture and a new SIMD Hands-on Morphological Image Processing

comparison of analog and digital fourier - Morphological image processing: Architecture and VLSI design Fourier transforms in medical image P. P. Jonker, Morphological image processing:

vlsi implementation of reconfigurable processing - M.E. VLSI Design, In the field of image processing, The reconfigurable module includes functions like basic mathematical morphological operations for

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flexible architectures for morphological image - An architecture for the efficient and high-speed realization of morphological filters is presented. Since morphological filtering can be described in terms of erosion

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